

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Attn: APPLICATION BRANCH**
Hisashi ADACHI et al. : Attorney Docket No. 2003-1782
Serial No. NEW :
Filed December 8, 2003 :

FREQUENCY SYNTHESIZER APPARATUS
EQUIPPED WITH FRACTION PART CONTROL
CIRCUIT, COMMUNICATION APPARATUS,
FREQUENCY MODULATOR APPARATUS,
AND FREQUENCY MODULATING METHOD
**(Rule 1.53(b) Divisional
of Serial No. 09/733,949,
Filed December 12, 2000)**

CLAIM OF PRIORITY UNDER 35 USC 119

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

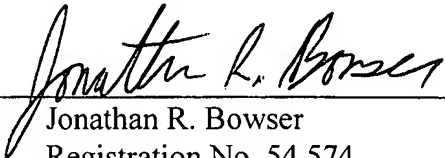
Sir:

Applicants in the above-entitled application hereby claim the date of priority under the International Convention of Japanese Patent Application No. P11-352964, filed December 13, 1999, as acknowledged in the Declaration of this application.

A certified copy of said Japanese Patent Application is of record in parent application Serial No. 09/733,949, filed December 12, 2000.

Respectfully submitted,

Hisashi ADACHI et al.

By 
Jonathan R. Bowser
Registration No. 54,574
Attorney for Applicants

JRB(JRF)/nk
Washington, D.C. 20006-1021
Telephone (202) 721-8200
Facsimile (202) 721-8250
December 8, 2003